

TAPERED DELAY LINE

ABSTRACT OF THE INVENTION

Thin film devices having conductors of non-uniform line width and line spacing between adjacent conductors at uncoupled regions of symmetrical conductive pathways. Several coil-shaped delay line circuits are disclosed wherein the innermost and outermost conductors exhibit different line width and spacing between adjoining conductors. The devices are constructed on rigid and flexible, folding substrates and necessary terminations are connected with solder filled vias and/or edge connections.